Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	3234	365/63	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:20
L6	369	rewriting adj2 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:20
L7	6228	reading adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:21
L8	3	5 AND 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:21
L9	1092	365/185.09	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:21
L10	1	6 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:21
L11	6065	365/201	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:21
L12	2	6 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:21
L13	1093	365/195	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:21

L14	0	6 and 13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:22
L15	802	tanaka-yoshiyuki:inv.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:22
L16	1	6 and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:22
S1	369	rewriting adj2 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 10:59
S2	6221	reading adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 10:49
S3	20	S1 and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ÖR	ON	2005/06/16 10:49
S4	43	(rewriting adj2 circuit) and (nonvolatile adj memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 11:24
S5	20	S4 and (connect\$3 near9 external\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 11:11
S6	61	S1 and (connect\$3 near9 external\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 11:23
\$7	8104	rewrit\$3 and (connect\$3 near9 external\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 11:24

S8	1236	S7 and (nonvolatile adj memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 11:24
S9	177	S8 and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 13:28
S10	8104	rewrit\$3 and (connect\$3 near9 external\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 13:28
S11	1236	S10 and (nonvolatile adj memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 13:28
S12	177	S11 and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 13:28
S13	141	S12 and writing	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	OR	ON	2005/06/16 13:30
S14	35	S12 and (functional adj block)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 16:20
S15	0	S14 and (external adj redundancy adj setting)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 14:06
S16	0	S14 and (external adj redundancy )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 14:06
S17	0	S14 and (external adj redundan\$3 )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/16 14:06

S18	3	S14 and (external near3 redundan\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT;	OR	ON	2005/06/16 14:06
			IBM_TDB			